**Makefile**

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1. what is make and makefile ?

2. what are target, dependencies and commands in Makefile ?

3. write a makefile for generating binary which have main.c add.c sub.c mul.c div.c files.

4. what all are the auto-variables ? How are they useful?

5. write a makefile illustrating the example of auto variables.

6. How to execute a makefile from another makefile ? Write a Makefile command.

7. What is phony target in makefile ?

8. What is the difference between makefile and Makefile?

9. Write a password security system in Makefile ?

10. Write a Makefile illustrating the example of Wildcard ?

11. what does mean by

make distclean make install

make clean make all

12. what are the macros in Makefile ? Write some examples for this.

13. explain use of include and override in Makefile.

14. what is the benefit of Makefile ?

15. generate a binary of any C program Using makefile and shell script.